

AMENDMENTS TO THE CLAIMS

This listing of claims replaces all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Original) In a computing system that includes one or more processors, persistent memory configured to store information that persists through power loss of the computing system, and system memory that may more directly accessed by the one or more processors, a method for recovering from a system failure, the method comprising the following:

an act of receiving a message corresponding to a particular message transaction following a message exchange pattern;

an act of loading state information for the message transaction from persistent memory to system memory in response to having received the message;

an act of determining from the state information whether or not the processing instance associated with the particular message transaction is in recovery mode; and

an act of branching process flow depending on whether or not the processing instance is in recovery mode.

2. (Original) A method in accordance with Claim 1, wherein the act of determining from the state information whether or not the processing instance associated with the particular message transaction is in recovery mode comprises the following:

an act of determining that the processing instance is in recovery mode.

3. (Original) A method in accordance with Claim 2, wherein the act of branching process flow depending on whether or not the processing instance is in recovery mode comprises the following:

an act of executing recovery code.

4. (Original) A method in accordance with Claim 3, further comprising the following:

after executing the recovery code, an act of determining that recovery has completed successfully.

5. (Original) A method in accordance with Claim 4, further comprising the following:

an act of causing the state information to reflect that the processing instance is no longer in recovery mode.

6. (Currently Amended) In a computing system that includes one or more processors, persistent memory configured to store information that persists through power loss of the computing system, and system memory that may more directly accessed by the one or more processors, a method for recovering from a system failure, the method comprising the following:

an act of receiving a message corresponding to a particular message transaction following a message exchange pattern;

an act of loading state information for the message transaction from persistent memory to system memory in response to having received the message;

an act of determining from the state information whether or not the processing instance associated with the particular message transaction is in recovery mode; and

an act of branching process flow depending on whether or not the processing instance is in recovery mode;

wherein the act of determining from the state information whether or not the processing instance associated with the particular message transaction is in recovery mode comprises an act of determining that the processing instance is in recovery mode;

wherein the act of branching process flow depending on whether or not the processing instance is in recovery mode comprises an act of executing recovery code; and~~A method in accordance with Claim 3,~~
wherein the act of executing of the recover code comprises the following:

an act of beginning execution of the recovery code;

an act of determining that further communication is needed in order to fully recover;

an act of storing the state information for the processing instance back into persistent memory upon determining that further communication is needed in order to fully recover;

an act of receiving a second message;

an act of reloading the state information from persistent memory to system memory in response to having received the second message;

an act of continuing execution of the recovery code upon reloading the state information.

7. (Original) A method in accordance with Claim 1, wherein the act of determining from the state information whether or not the processing instance associated with the particular transaction is in recovery mode comprises the following:

an act of determining that the processing instance is in normal mode.

8. (Original) A method in accordance with Claim 7, wherein the act of branching process flow depending on whether or not the processing instance is in recovery mode comprises the following:

an act of executing normal code without executing recovering code.

9. (Currently Amended) ~~A method in accordance with Claim 1, In a computing system that includes one or more processors, persistent memory configured to store information that persists through power loss of the computing system, and system memory that may more directly accessed by the one or more processors, a method for recovering from a system failure, the method comprising the following:~~

an act of receiving a message corresponding to a particular message transaction following a message exchange pattern;

an act of loading state information for the message transaction from persistent memory to system memory in response to having received the message;

an act of determining from the state information whether or not the processing instance associated with the particular message transaction is in recovery mode;

an act of branching process flow depending on whether or not the processing instance is in recovery mode; and

wherein the message is a first message, the message transaction is a first message transaction, the processing instance is a first processing instance, and the state information is first state information, the method further comprising the following:

an act of receiving a second message of a second message transaction;

an act of loading second state information for the second message transaction from persistent memory to system memory in response to having received the second message; and

an act of determining from the second state information whether or not the processing instance associated with the second message is in recovery mode.

10. (Original) A method in accordance with Claim 9, wherein the message exchange pattern is a first message exchange pattern, wherein the second message transaction is in accordance with a second message exchange pattern that is different than the first message exchange pattern.

11. (Original) A method in accordance with Claim 9, wherein the second message transaction is in accordance with the message exchange pattern of the first message.

12. (Original) A method in accordance with Claim 9, wherein the first state information indicates that the first processing instance is in recovery mode, while at the same time the second state information indicates that the second processing instance is not in recovery mode.

13. (Currently Amended) ~~A method in accordance with Claim 1;~~ In a computing system that includes one or more processors, persistent memory configured to store information that persists through power loss of the computing system, and system memory that may more directly accessed by the one or more processors, a method for recovering from a system failure, the method comprising the following:

an act of receiving a message corresponding to a particular message transaction following a message exchange pattern;

an act of loading state information for the message transaction from persistent memory to system memory in response to having received the message;

an act of determining from the state information whether or not the processing instance associated with the particular message transaction is in recovery mode;

an act of branching process flow depending on whether or not the processing instance is in recovery mode;

~~further comprising the following:~~

an act of identifying a plurality of processing instances that were running at the time of a system failure; and

an act of indicating in state information associated with each of the identified plurality of processing instances that the corresponding processing instance is in recovery mode, wherein the plurality of processing instances includes the processing instance.

14. (Currently Amended) A computer program product for use in a computing system that includes one or more processors, persistent memory configured to store information that persists through power loss of the computing system, and system memory that may more directly accessed by the one or more processors, a computer program product for implementing a method for recovering from a system failure, the computer program product comprising one or more recordable-type computer-readable storage media having thereon computer-executable instructions that, when executing by the one or more processors, cause the computing system to perform the following:

- an act of detecting receipt of a message corresponding to a particular message transaction that follows a message exchange pattern;

- an act of loading state information for the message transaction from persistent memory to system memory in response to having received the message;

- an act of determining from the state information whether or not the processing instance associated with the particular message transaction is in recovery mode; and

- an act of branching process flow depending on whether or not the processing instance is in recovery mode.

15. (Original) A computer program product in accordance with Claim 14, wherein the computer-executable instructions for performing the act of determining from the state information whether or not the processing instance associated with the particular message transaction is in recovery mode comprise computer-executable instructions that, when executed by the one or more processors, cause the computing system to perform the following:

- an act of determining that the processing instance is in recovery mode.

16. (Original) A computer program product in accordance with Claim 15, wherein the computer-executable instructions for performing the act of branching process flow depending on whether or not the processing instance is in recovery mode comprise computer-executable instructions that, when executed by the one or more processors, cause the computing system to perform the following:

- an act of executing recovery code.

17. (Original) A computer program product in accordance with Claim 16, wherein the one or more computer-readable media further have thereon computer-executable instructions that, when executed by the one or more processors, cause the computing system to further perform the following:

- after executing the recovery code, an act of determining that recovery has completed successfully.

18. (Original) A computer program product in accordance with Claim 17, wherein the one or more computer-readable media further have thereon computer-executable instructions that, when executed by the one or more processors, cause the computing system to further perform the following:

an act of causing the state information to reflect that the processing instance is no longer in recovery mode.

19. (Original) A computer program product in accordance with Claim 16, wherein the computer-executable instructions for perform the act of executing of the recover code comprise computer-executable instructions that, when executed by the one or more processors, cause the computing system to perform the following:

an act of beginning execution of the recovery code;

an act of determining that further communication is needed in order to fully recover;

an act of storing the state information for the processing instance back into persistent memory upon determining that further communication is needed in order to fully recover;

an act of receiving a second message;

an act of reloading the state information from persistent memory to system memory in response to having received the second message;

an act of continuing execution of the recovery code upon reloading the state information.

20. (Original) A computer program product in accordance with Claim 14, wherein the computer-executable instructions for performing the act of determining from the state information whether or not the processing instance associated with the particular message transaction is in recovery mode comprise computer-executable instructions that, when executed by the one or more processors, cause the computing system to perform the following:

an act of determining that the processing instance is in normal mode.

21. (Original) A computer program product in accordance with Claim 20, wherein the computer-executable instructions for performing the act of branching process flow depending on whether or not the processing instance is in recovery mode comprise computer-executable instructions that, when executed by the one or more processors, cause the computing system to perform the following:

an act of executing normal code without executing recovering code.

22. (Original) A computer program product in accordance with Claim 14, wherein the message is a first message, the message transaction is a first message transaction, the processing instance is a first processing instance, and the state information is first state information, the one or more computer-readable media further having thereon computer-executable instructions that, when executed by the one or more processors, cause the computing system to perform the following:

- an act of detecting receipt of a second message of a second message exchange pattern;
- an act of loading second state information for the second message transaction from persistent memory to system memory in response to having received the second message; and
- an act of determining from the second state information whether or not the processing instance associated with the second message is in recovery mode.

23. (Original) A computer program product in accordance with Claim 22, wherein the message exchange pattern is a first message exchange pattern, wherein the second message transaction is in accordance with a second message exchange pattern that is different than the first message exchange pattern.

24. (Original) A computer program product in accordance with Claim 22, wherein the second message transaction is in accordance with the message exchange pattern of the first message.

25. (Original) A computer program product in accordance with Claim 22, wherein the first state information indicates that the first processing instance is in recovery mode, while at the same time the second state information indicates that the second processing instance is not in recovery mode.

26. (Original) A computer program product in accordance with Claim 14, wherein the one or more computer-readable media further have thereon computer-executable instructions that, when executed by the one or more processors, cause the computing system to further perform the following:

- an act of identifying a plurality of processing instances that were running at the time of a system failure; and
- an act of indicating in state information associated with each of the identified plurality of processing instances that the corresponding processing instance is in recovery mode, wherein the plurality of processing instances includes the processing instance.

27. (Original) One or more computer-readable media in accordance with Claim 14, wherein the one or more computer-readable media are physical memory media.

28. (Original) One or more computer-readable media in accordance with Claim 27, wherein the physical memory media is system memory.

29. (Original) One or more computer-readable media in accordance with Claim 27, wherein the physical memory media is persistent media.

30. (Original) In a computing system that includes one or more processors, persistent memory configured to store information that persists through power loss of the computing system, and system memory that may more directly accessed by the one or more processors, a method for recovering from a system failure, the method comprising the following:

- an act of detecting receipt of a message corresponding to a particular message transaction pattern that follows a message exchange pattern; and

- a step for identifying an operational mode of a processing instance corresponding to the particular message transaction.

31. (Original) A method in accordance with Claim 30, wherein the step for identifying an operational mode of a processing instance corresponding to the particular message transaction comprises the following:

- an act of loading state information for the message transaction from persistent memory to system memory in response to having received the message;

- an act of determining from the state information whether or not the processing instance associated with the particular message transaction is in recovery mode; and

- an act of branching process flow depending on whether or not the processing instance is in recovery mode.

32. (Currently Amended) A computer program product for use in a computing system that includes one or more processors, persistent memory configured to store information that persists through power loss of the computing system, and system memory that may more directly accessed by the one or more processors, a computer program product for comprising one or more recordable-type computer-readable storage media having thereon computer-executable instructions that, when executing by the one or more processors, cause the computing system to instantiate in the system memory the following:

a plurality of processing instances, each associated with a particular message transaction, each processing instance being configured to load state information for the message transaction from persistent memory to system memory in response to having received a message for the message transaction, determine from the state information whether or not the processing instance associated with the particular message transaction is in recovery mode, and branch process flow depending on whether or not the processing instance is in recovery mode; and

a recovery detection component configured to identify the plurality of processing instances as running at the time of a system failure; and indicating in state information associated with each of the identified plurality of processing instances that the corresponding processing instance is in recovery mode.